

In re application of: NAKAMURA, Shunji, et al.

Serial Number: 10/708,792

Filed: March 25, 2004

For. SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING

**SAME** 

## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

March 25, 2004

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and

Trademark Office the references listed on the attached PTO-1449.

## Reference AI is discussed at page 5 of the specification.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (5)

INFORMATION Atty. Docket No. 040126 Serial No. 10/708,792

DISCLOSURE STATEMENT PTO-1449 Filing Date: March 25, 2004 Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

## Examiner Initial Document No. Name Date Class Subclass Filing Date (If appropriate) AA AB

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AF Patent Abstracts of Japan No. 08-186249 (Abstract)

AG Patent Abstracts of Japan No. 05-121450 (Abstract)

AH Patent Abstracts of Japan No. 2000-012791 (Abstract)

AI Patent Abstracts of Japan No. 2000-022158 (Abstract)

AJ Patent Abstracts of Japan No. 2000-286418 (Abstract)

Examiner

Date Considered